

## Detailed Action

### Comments of the Examiner

This application contains claims directed to the following patentably distinct species of the claimed invention:

I. An Active Pixel CMOS image having a transistor for resetting the body of another transistor correspond to claims 1-6.

II. An Active Pixel CMOS image having an array of pixels and a column signal bus line correspond to claims 7-14.

Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is finally held to be allowable. Currently, none of the claims is generic.

Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which are written in dependent form or otherwise include all the limitations of an allowed generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species. MPEP § 809.02(a).

Should applicant traverse on the ground that the species are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the species to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C. 103(a) of the other invention.

### Reply to the Examiner's Comments

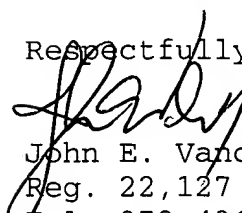
Applicant , with traverse, **elects Group 1, claims 1-6** directed to: An Active Pixel CMOS image having a transistor for resetting the body of another transistor .

Applicants contends while claims 1-6 are directed to an active pixel CMOS device including at least one pixel.

Claims 7-14 are directed to An Active Pixel CMOS image sensor device including at least one pixel circuit. This at least one pixel circuit may be the active pixel cmos image sensor device including at least one pixel circuit of Claim 1.

A copy of the elected claims, 1-6, are set forth above.

Respectfully submitted,

  
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